

### Features

20V, 2.3A  
 $R_{DS(ON)} < 60m\Omega @ V_{GS} = 4.5V$   
 $R_{DS(ON)} < 90m\Omega @ V_{GS} = 2.5V$   
 Advanced Trench Technology  
 Excellent  $R_{DS(ON)}$  and Low Gate Charge  
 Lead Free

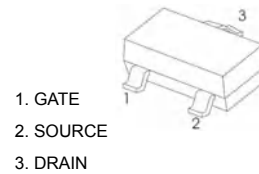
### Applications

Load Switch  
 PWM Application  
 Power Management

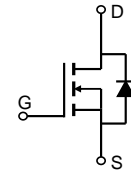
$V_{DSS}$  20 V  
 $I_D$  2.3 A  
 $R_{DS(ON)}$  45 m $\Omega$

A2SHB.

### SOT-23



### Equivalent Circuit



## MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Maximum ratings ( $T_a=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	20	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 12$	V
$I_D$	Continuous Drain Current	$T_A = 25^\circ\text{C}$	2.3
		$T_A = 100^\circ\text{C}$	1.3
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	12	A
$P_D$	Power Dissipation	$T_A = 25^\circ\text{C}$	1.2
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient <sup>(2)</sup>	103	$^\circ\text{C/W}$
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	$^\circ\text{C}$

Electrical characteristics ( $T_a=25^{\circ}\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	20	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}$	-	-	1.0	$\mu\text{A}$
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 12\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	0.5	-	1.0	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance <sup>(3)</sup>	$V_{GS} = 4.5\text{V}, I_D = 3\text{A}$	-	45	60	$\text{m}\Omega$
		$V_{GS} = 2.5\text{V}, I_D = 2\text{A}$	-	65	90	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 10\text{V},$ $f = 1\text{MHz}$	-	200	-	pF
$C_{oss}$	Output Capacitance		-	35	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	28	-	pF
$Q_g$	Total Gate Charge	$V_{GS} = 0$ to $4.5\text{V}$ $V_{DD} = 10\text{V}, I_D = 2\text{A}$	-	3	-	nC
$Q_{gs}$	Gate Source Charge		-	0.5	-	nC
$Q_{gd}$	Gate Drain("Miller") Charge		-	0.7	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On DelayTime	$V_{GS} = 4.5\text{V}, V_{DD} = 10\text{V}$ $I_D = 2\text{A}, R_{GEN} = 3\Omega$	-	3	-	ns
$t_r$	Turn-On Rise Time		-	11	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	20	-	ns
$t_f$	Turn-Off Fall Time		-	8	-	ns
<b>Drain-Source Diode Characteristics and Max Ratings</b>						
$I_S$	Maximum Continuous Drain to Source Diode Forward Current		-	-	3	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current		-	-	12	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = 3\text{A}$	-	-	1.2	V
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F = 2\text{A}, di/dt = 100\text{A}/\mu\text{s}$	-	4.3	-	ns
$Q_{rr}$	Body Diode Reverse Recovery Charge		-	0.6	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2.  $R_{\theta JA}$  is measured with the device mounted on a  $1\text{inch}^2$  pad of 2oz copper FR4 PCB
  3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$ .

RATING AND CHARACTERISTIC CURVES

Figure 1: Output Characteristics

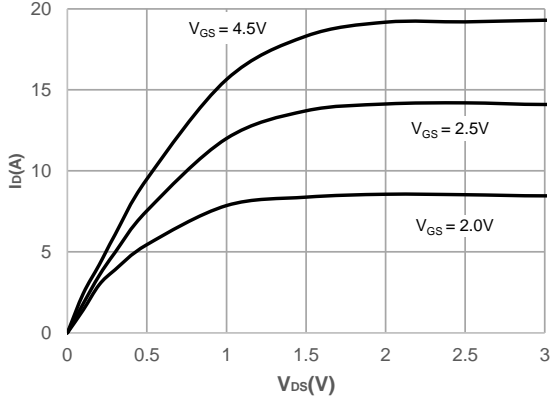


Figure 2: Typical Transfer Characteristics

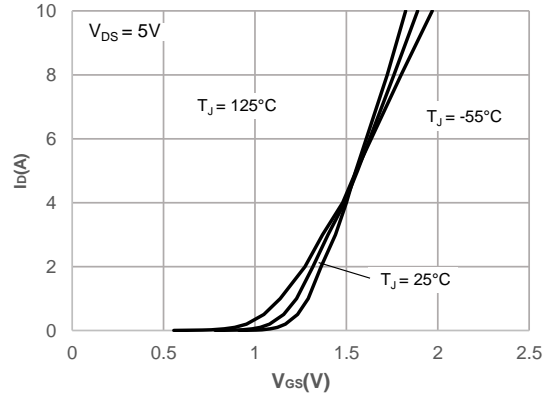


Figure 3: On-resistance vs. Drain Current

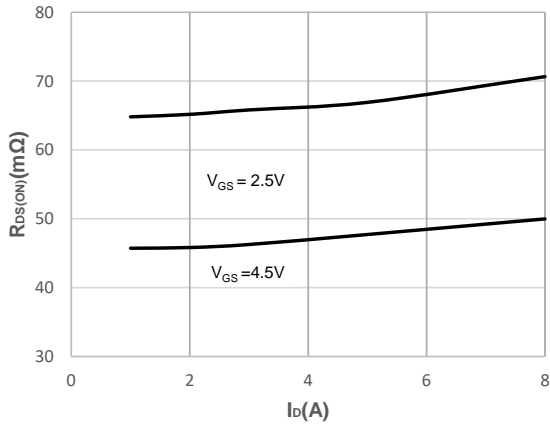


Figure 4: Body Diode Characteristics

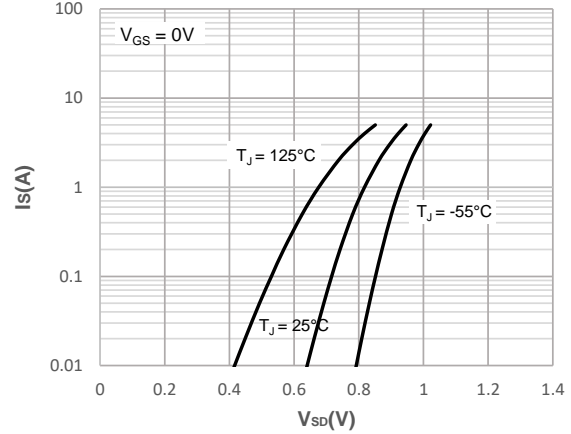


Figure 5: Gate Charge Characteristics

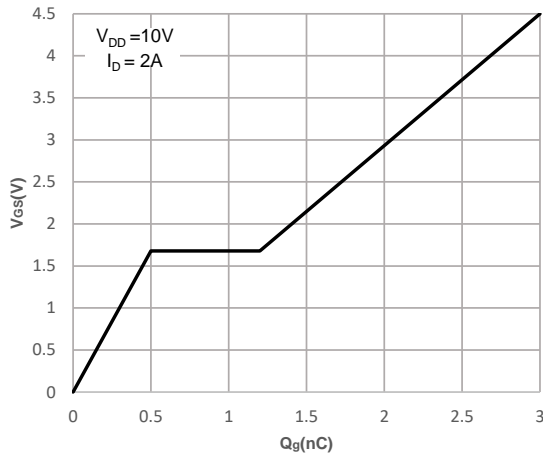
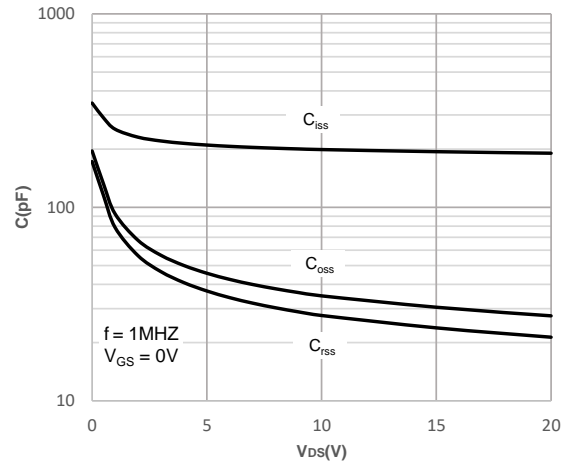
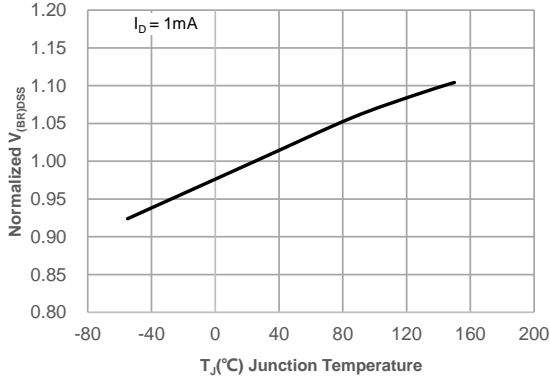


Figure 6: Capacitance Characteristics

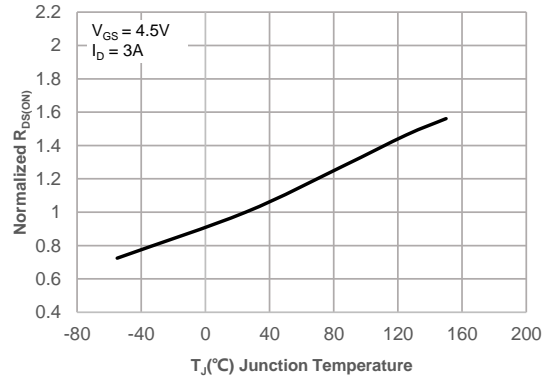


**RATING AND CHARACTERISTIC CURVES**

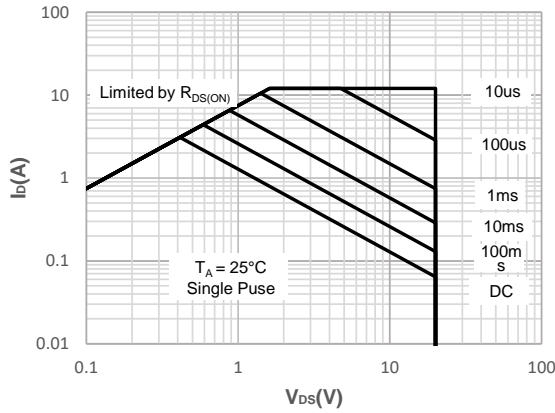
**Figure 7: Normalized Breakdown voltage vs. Junction Temperature**



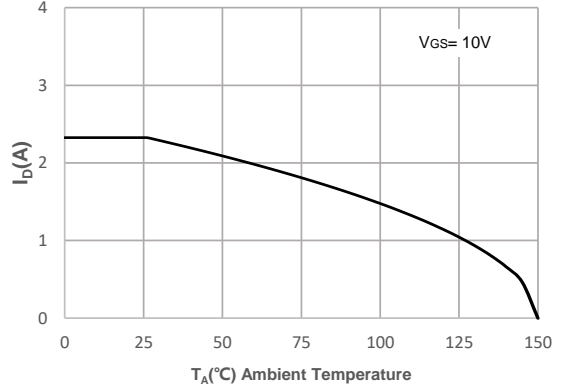
**Figure 8: Normalized on Resistance vs. Junction Temperature**



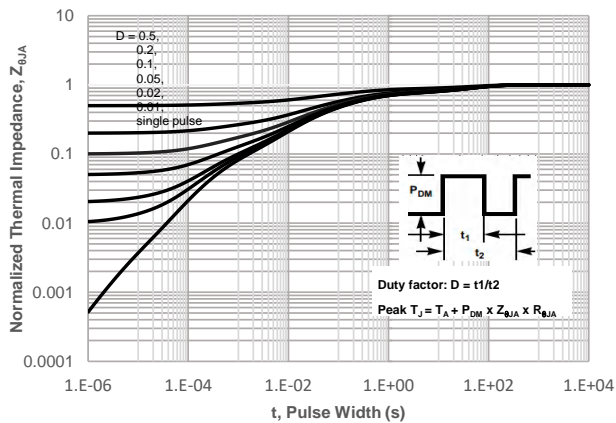
**Figure 9: Maximum Safe Operating Area**



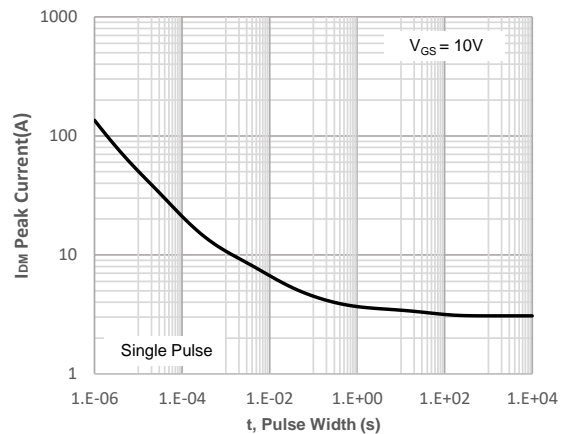
**Figure 10: Maximum Continuous Driand Current vs. Ambient Temperature**



**Figure 11: Normalized Maximum Transient Thermal Impedance**



**Figure 12: Peak Current Capacity**



Test Circuit

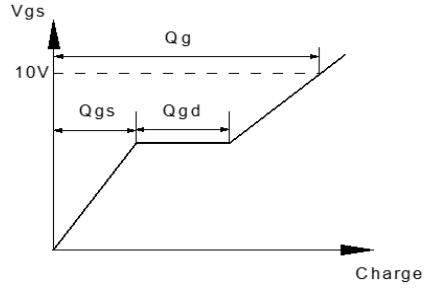
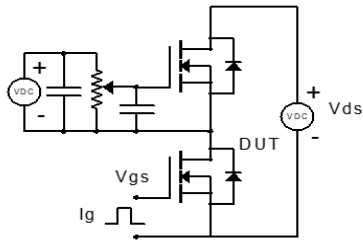


Figure 1: Gate Charge Test Circuit & Waveform

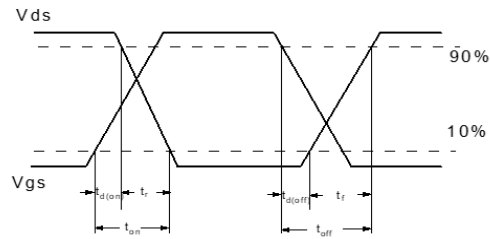
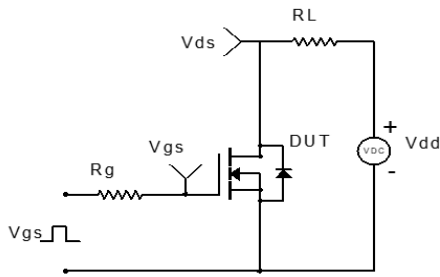


Figure 2: Resistive Switching Test Circuit & Waveform

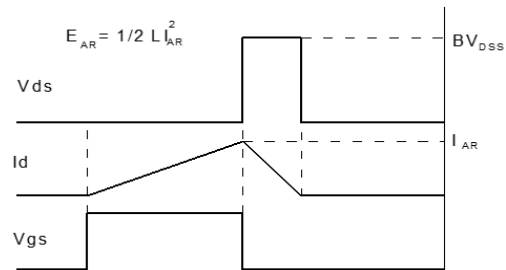
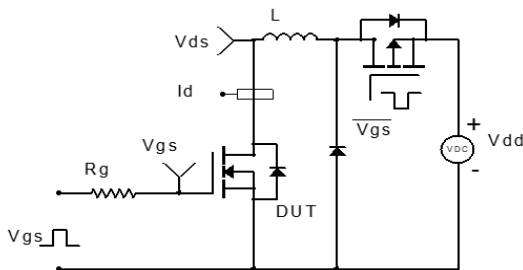


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

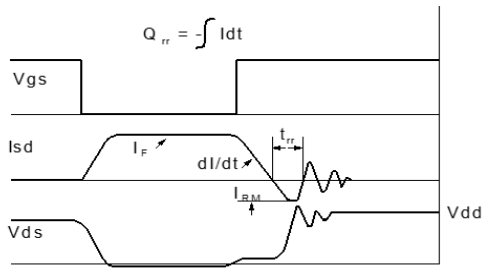
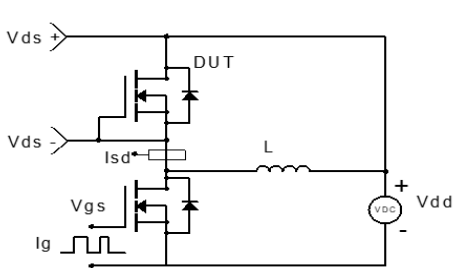
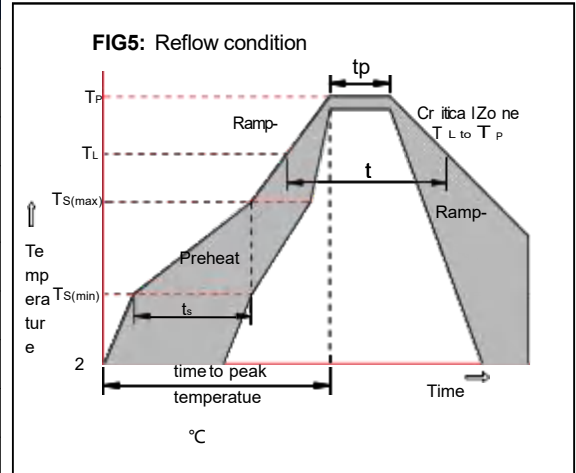


Figure 4: Diode Recovery Test Circuit & Waveform

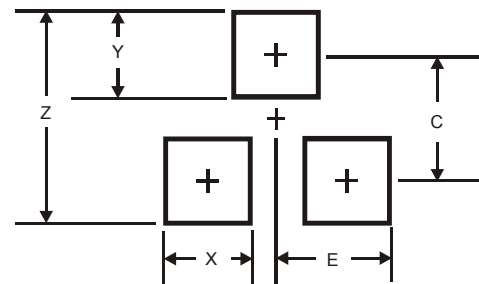
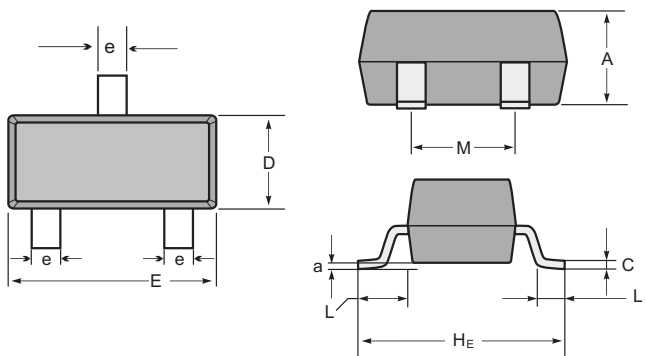
Soldering parameters

Reflow Condition		Pb-Free assembly (see as below)
Pre Heat	-Temperature Min ( $T_{s(min)}$ )	+150°C
	-Temperature Max( $T_{s(max)}$ )	+200°C
	-Time (Min to Max) (ts)	60-180 secs.
Average ramp up rate (Liquid us Temp ( $T_L$ ) to peak)		3°C/sec. Max
$T_{s(max)}$ to $T_L$ - Ramp-up Rate		3°C/sec. Max
Reflow	-Temperature( $T_L$ )(Liquid us)	+217°C
	-Temperature( $t_L$ )	60-150 secs.
Peak Temp ( $T_P$ )		+260(+0/-5)°C
Time within 5°C of actual Peak Temp ( $t_p$ )		30 secs. Max
Ramp-down Rate		6°C/sec. Max
Time 25°C to Peak Temp ( $T_P$ )		8 min. Max
Do not exceed		+260°C



Package Dimensions & Suggested Pad Layout

SOT23



SOT-23 mechanical data

UNIT	A	C	D	E	$H_E$	e	M	L	$L_1$	a	
mm	max	1.1	0.15	1.4	3.0	2.6	0.5	1.95	0.55 (ref)	0.36 (ref)	0.0
	min	0.9	0.08	1.2	2.8	2.2	0.3	1.7			0.15
mil	max	43	6	55	118	102	20	77	22 (ref)	14 (ref)	0.0
	min	35	3	47	110	87	12	67			6

Dimensions	SOT23
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

Tape & reel specification

Tape		Symbol	Dimension (mm)
		P0	4.00±0.10
		P1	4.00±0.10
		P2	2.00±0.10
		D0	1.55±0.10
		D1	1.05±0.10
		E	1.55±0.10
		F	3.60±0.10
		W	8.00±0.10
		A0	3.80±0.20
		B0	3.25±0.20
		K0	1.45±0.10
		T	0.25±0.05
		D2	178.0±3.0
		D3	55Min.
		D4	R24.0±3.0
G	R82.0±3.0		
I	13.0±2.0		
W1	11.0±3.0		
Quantity: 3000PCS			

7" Reel

