

### Description

The AO3422 is the high cell density trenched N-ch MOSFETs, which provides excellent R<sub>DS(ON)</sub> and efficiency for most of the small power switching and load switch applications.

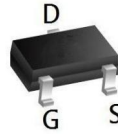
The AO3422 meet the RoHS and Green Product requirement with full function reliability approved.

### Features

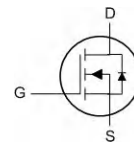
Green Device Available  
Super Low Gate Charge  
Excellent CdV/dt effect decline  
Advanced high cell density Trench technology

$V_{DSS}$  60 V  
 $I_D$  3 A  
 $R_{DS(ON)}$  75 mΩ

3422



SOT23-3L top view



Schematic Diagram

## MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	60	V
$V_{GS}$	Gate-Source Voltage	±20	V
$I_D @ T_A=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	3.0	A
$I_D @ T_A=70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	1.8	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	9.2	A
$P_D @ T_A=25^\circ\text{C}$	Total Power Dissipation <sup>3</sup>	1	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	---	125	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	80	°C/W

**Electrical characteristics ( $T_J=25^{\circ}\text{C}$  unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	60	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25^{\circ}\text{C}$ , $I_D=1\text{mA}$	---	0.054	---	$\text{V}/^{\circ}\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=10V, I_D=2A$	---	75	100	m $\Omega$
		$V_{GS}=4.5V, I_D=1A$	---	85	120	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	---	2.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-4.96	---	$\text{mV}/^{\circ}\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=48V, V_{GS}=0V, T_J=25^{\circ}\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=48V, V_{GS}=0V, T_J=55^{\circ}\text{C}$	---	---	5	
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA
$g_{fs}$	Forward Transconductance	$V_{DS}=5V, I_D=2A$	---	13	---	S
$Q_g$	Total Gate Charge (4.5V)	$V_{DS}=48V, V_{GS}=4.5V, I_D=2A$	---	5	7.0	nC
$Q_{gs}$	Gate-Source Charge		---	1.68	2.4	
$Q_{gd}$	Gate-Drain Charge		---	1.9	2.7	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=30V, V_{GS}=10V, R_G=3.3\Omega, I_D=2A$	---	1.6	3.2	ns
$T_r$	Rise Time		---	7.2	13	
$T_{d(off)}$	Turn-Off Delay Time		---	25	50	
$T_f$	Fall Time		---	14.4	28.8	
$C_{iss}$	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1\text{MHz}$	---	511	715	pF
$C_{oss}$	Output Capacitance		---	38	53	
$C_{riss}$	Reverse Transfer Capacitance		---	25	35	

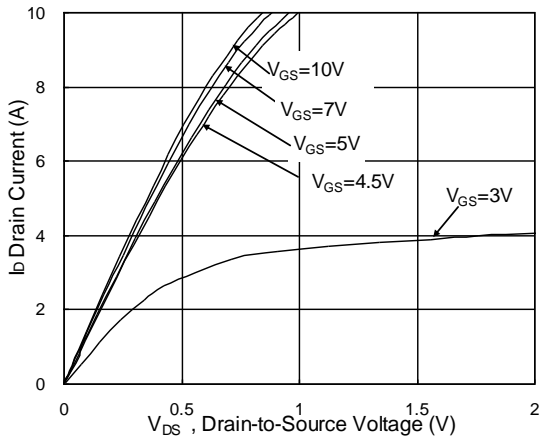
**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current <sup>1,4</sup>	$V_G=V_D=0V$ , Force Current	---	---	2.3	A
$I_{SM}$	Pulsed Source Current <sup>2,4</sup>		---	---	9.2	A
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0V, I_S=1A, T_J=25^{\circ}\text{C}$	---	---	1.2	V
$t_{rr}$	Reverse Recovery Time	$I_F=2A, dI/dt=100\text{A}/\mu\text{s}, T_J=25^{\circ}\text{C}$	---	9.7	---	nS
$Q_{rr}$	Reverse Recovery Charge		---	5.8	---	nC

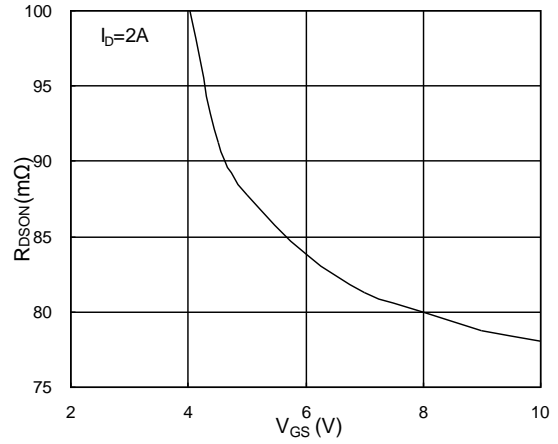
Note :

- The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$
- The power dissipation is limited by  $150^{\circ}\text{C}$  junction temperature.
- The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.

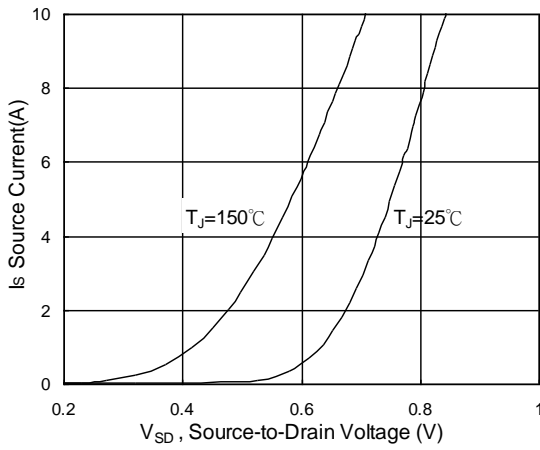
RATING AND CHARACTERISTIC CURVES



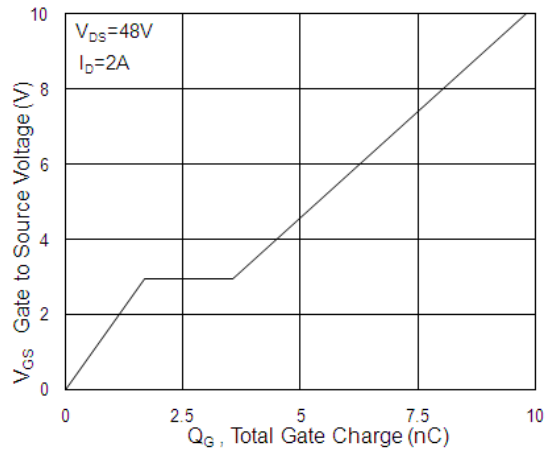
**Fig.1 Typical Output Characteristics**



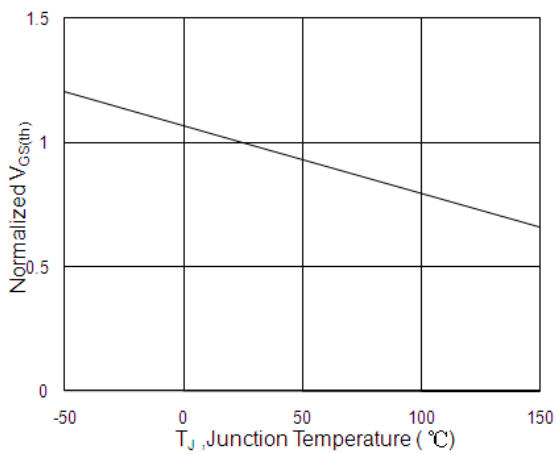
**Fig.2 On-Resistance v.s Gate-Source**



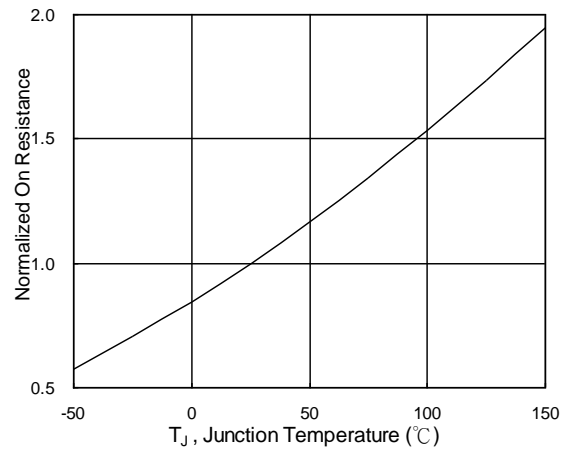
**Fig.3 Forward Characteristics of Reverse**



**Fig.4 Gate-Charge Characteristics**

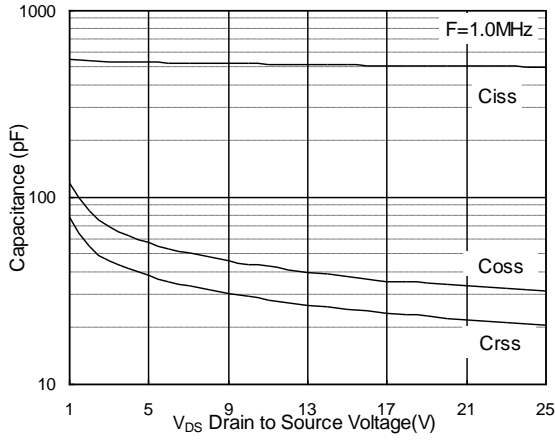


**Fig.5 Normalized  $V_{GS(th)}$  v.s  $T_J$**

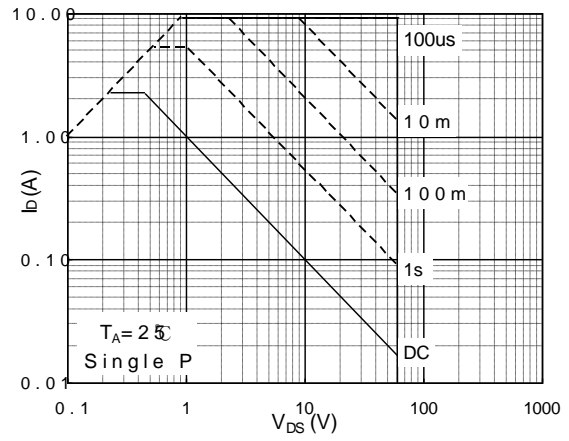


**Fig.6 Normalized  $R_{DS(on)}$  v.s  $T_J$**

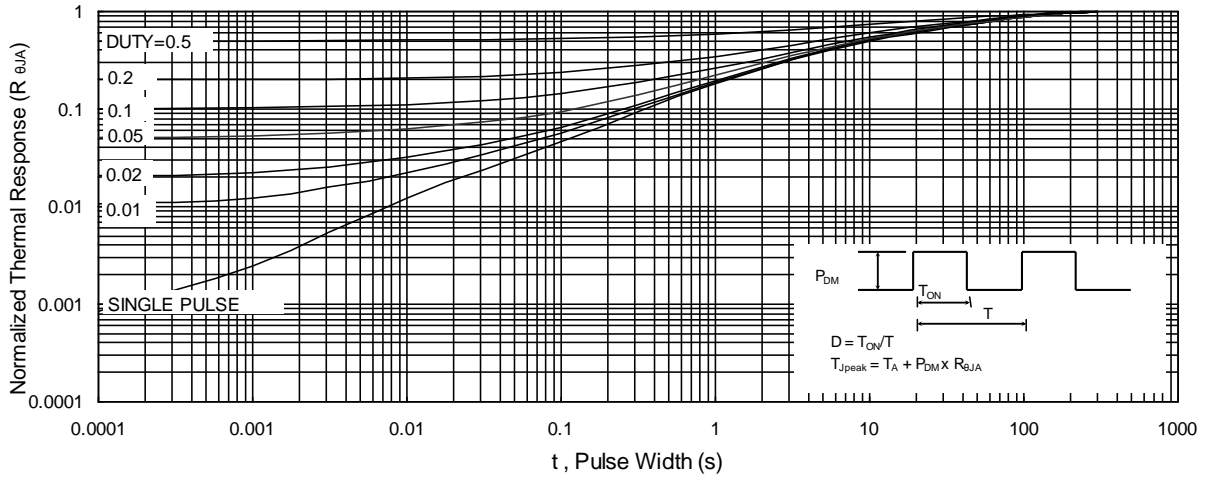
**RATING AND CHARACTERISTIC CURVES**



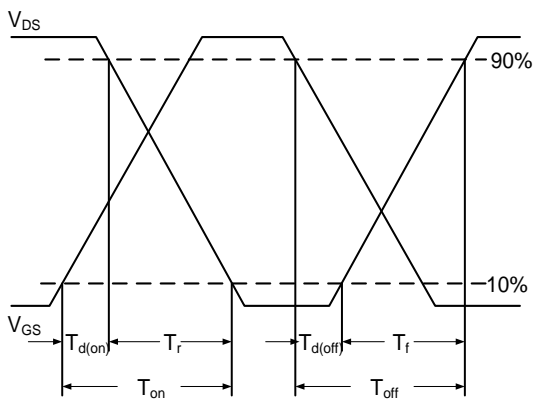
**Fig.7 Capacitance**



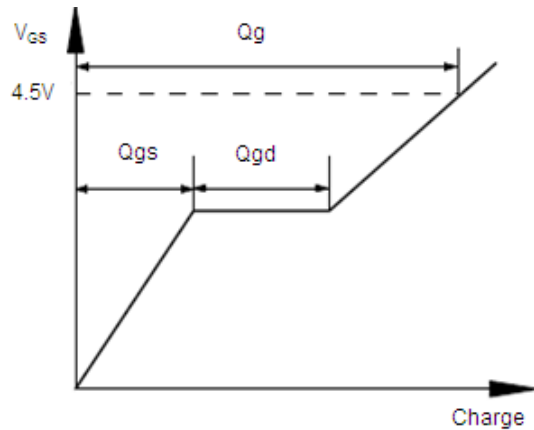
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



**Fig.10 Switching Time Waveform**



**Fig.11 Gate Charge Waveform**

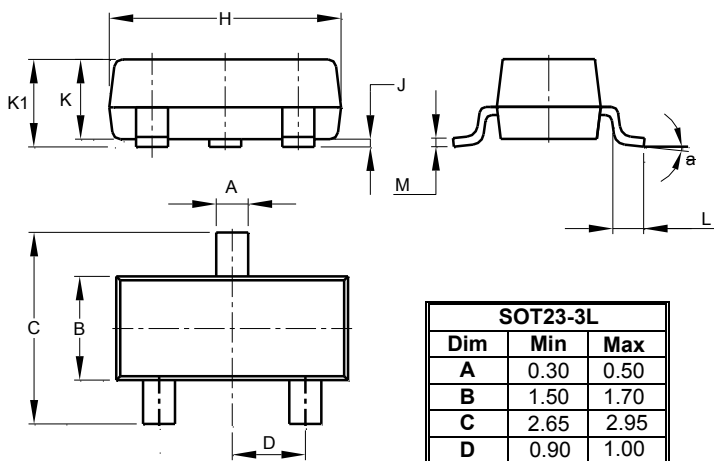
Soldering parameters

Reflow Condition		Pb-Free assembly (see as below)
Pre Heat	-Temperature Min ( $T_{s(min)}$ )	+150°C
	-Temperature Max( $T_{s(max)}$ )	+200°C
	-Time (Min to Max) (ts)	60-180 secs.
Average ramp up rate (Liquid us Temp ( $T_L$ ) to peak)		3°C/sec. Max
$T_{s(max)}$ to $T_L$ - Ramp-up Rate		3°C/sec. Max
Reflow	-Temperature( $T_L$ )(Liquid us)	+217°C
	-Temperature( $t_L$ )	60-150 secs.
Peak Temp ( $T_P$ )		+260(+0/-5)°C
Time within 5°C of actual Peak Temp ( $t_p$ )		30 secs. Max
Ramp-down Rate		6°C/sec. Max
Time 25°C to Peak Temp ( $T_P$ )		8 min. Max
Do not exceed		+260°C



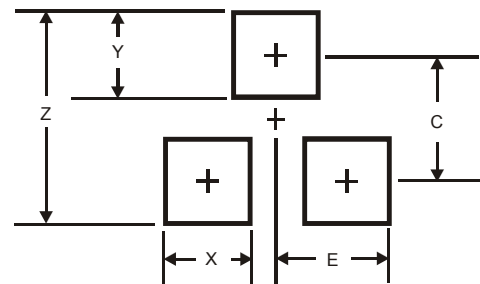
Package Dimensions & Suggested Pad Layout

SOT23-3L



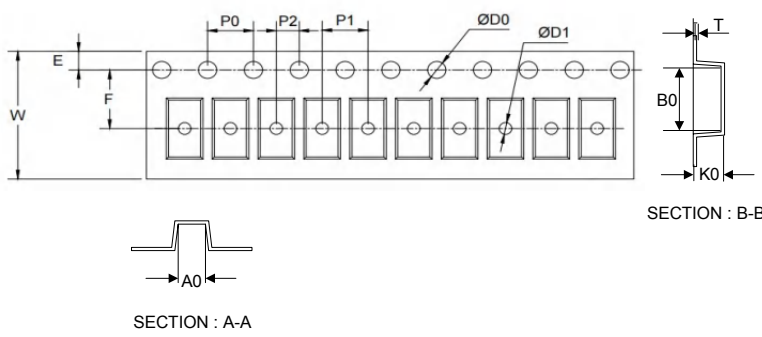
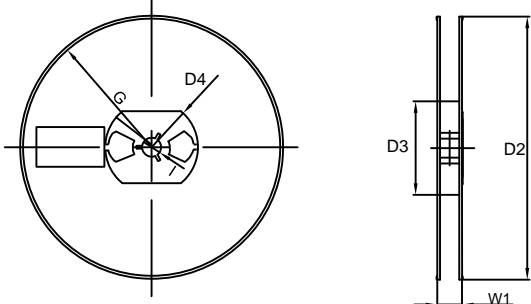
SOT23-3L		
Dim	Min	Max
A	0.30	0.50
B	1.50	1.70
C	2.65	2.95
D	0.90	1.00
H	2.82	3.02
J		0.10
K	1.05	1.15
K1	1.05	1.25
L	0.30	0.60
M	0.10	0.20
a	0°	8°

All Dimensions in mm



Dimensions	SOT23-3L
Z	3.3
X	0.9
Y	1.0
C	2.3
E	1.40

Tape & reel specification

Tape	Symbol	Dimension (mm)	
	P0	4.00±0.20	
	P1	4.00±0.20	
	P2	2.00±0.20	
	D0	1.55±0.20	
	D1	1.05±0.20	
	E	1.55±0.20	
	F	3.60±0.20	
	W	8.00±0.20	
	A0	3.80±0.20	
	B0	3.50±0.20	
	K0	1.55±0.20	
	T	0.25±0.15	
	<p>7" Reel</p> 	D2	178.0±5.0
		D3	55Min.
D4		R24.0±3.0	
G		R82.0±3.0	
I		13.0±2.0	
W1		11.0±3.0	
Quantity: 3000PCS			